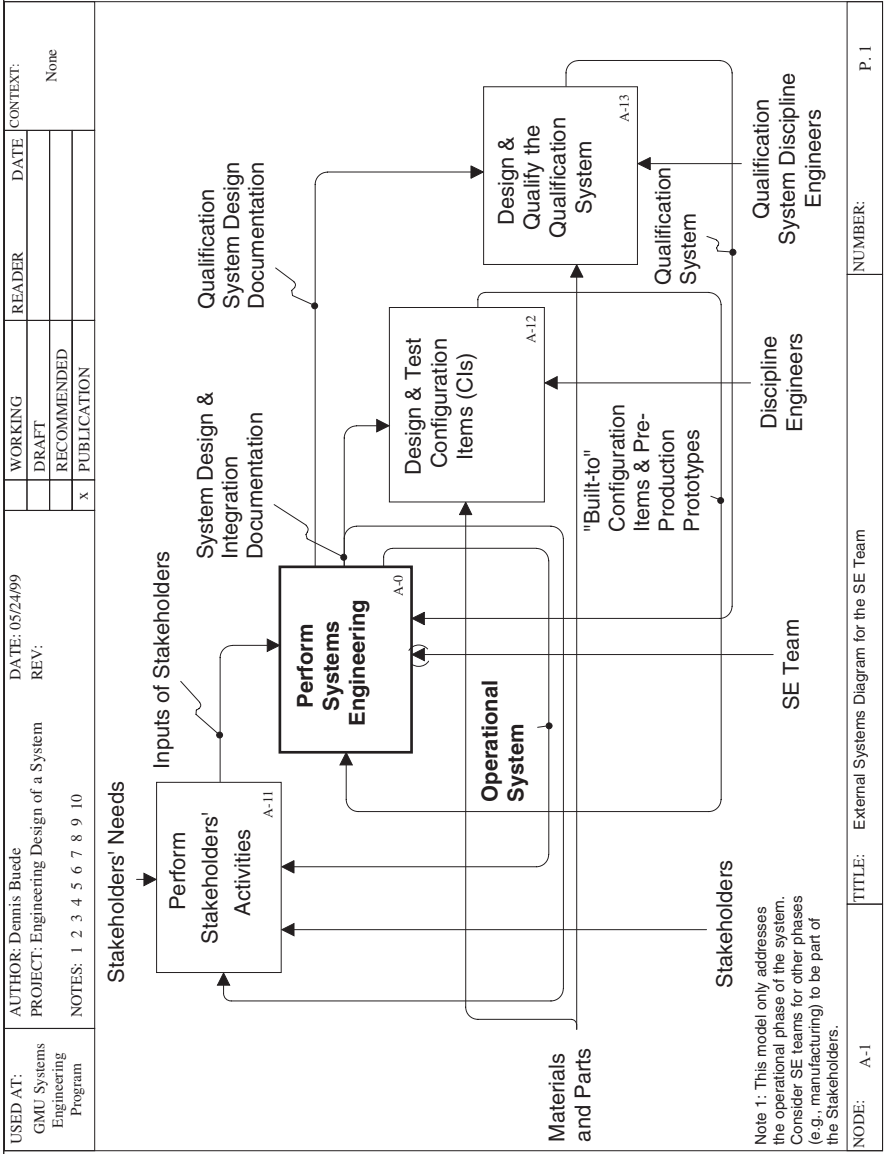


## Appendix B

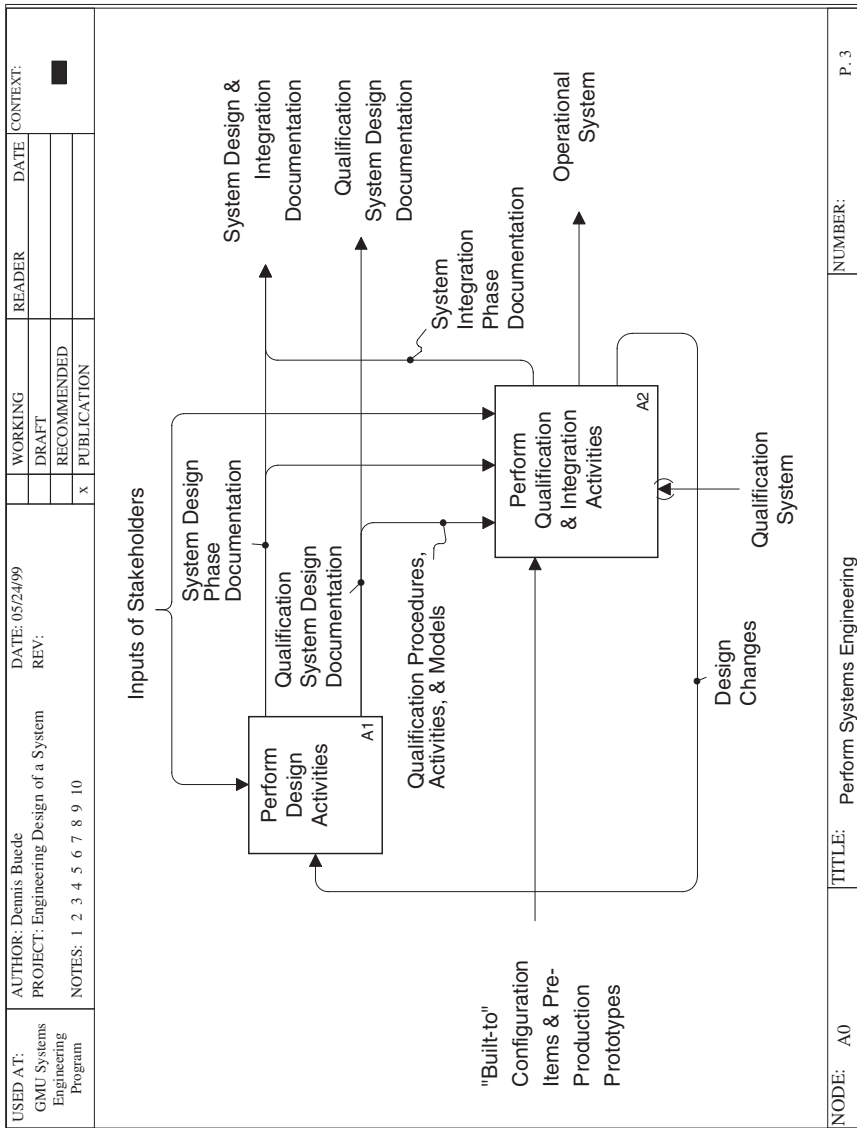
---

# IDEF0 Model of the Engineering of a System

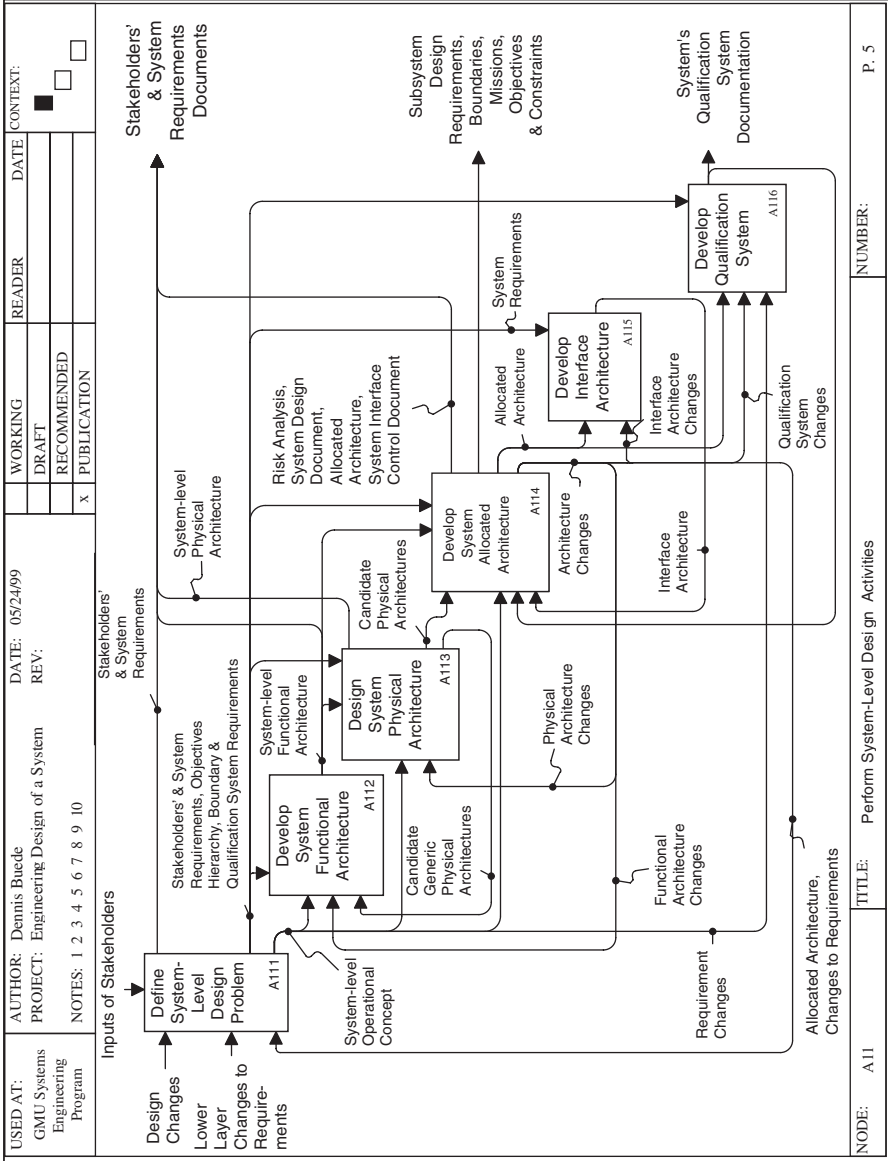
---



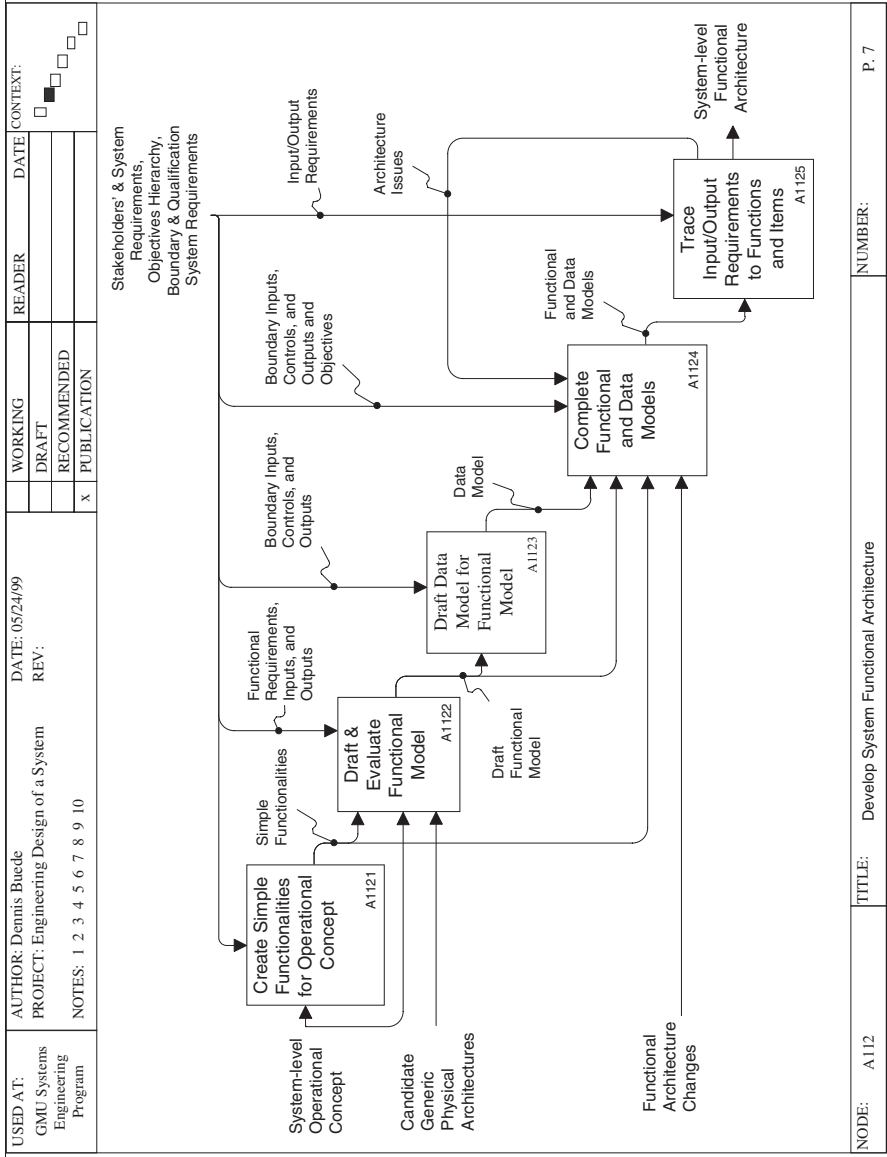
USED AT: GMU Systems Engineering Program	AUTHOR: Dennis Buede PROJECT: Engineering Design of a System NOTES: 1 2 3 4 5 6 7 8 9 10	DATE: 05/24/99 REV:	WORKING DRAFT RECOMMENDED PUBLICATION	READER	DATE	CONTEXT: Top
<div style="text-align: center;"> <pre>                     graph TD                         A[Inputs of Stakeholders] --&gt; B[Perform Systems Engineering]                         C["Built-to" Configuration Items &amp; Pre-Production Prototypes] --&gt; B                         D[Qualification System] --&gt; B                         B --&gt; E[System Design &amp; Integration Documentation]                         B --&gt; F[Operational System]                         B --&gt; G[Qualification System Design Documentation]                     </pre> </div> <p><b>PURPOSE: To describe the systems engineering process</b></p> <p><b>VIEWPOINT: The Systems Engineering Team</b></p>						
NODE: A-0	TITLE: Perform Systems Engineering	NUMBER:	P. 2			



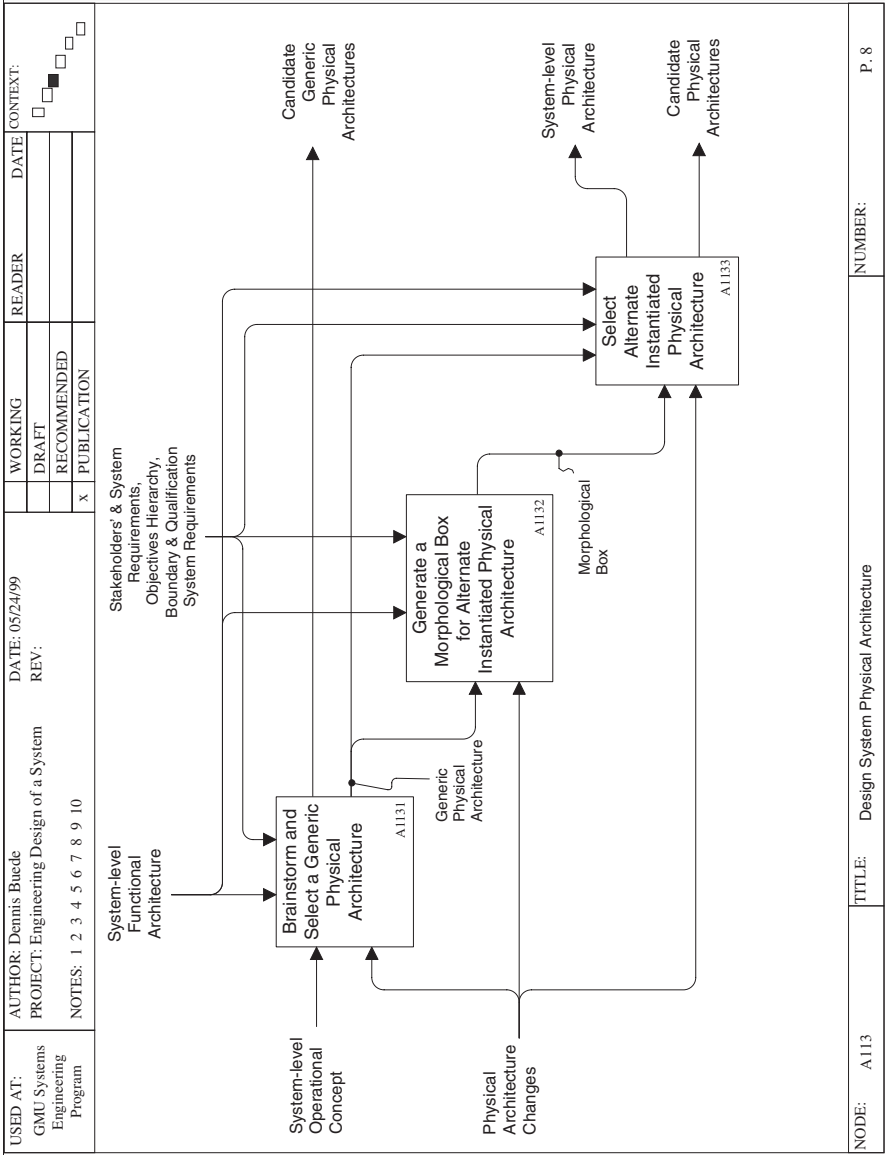


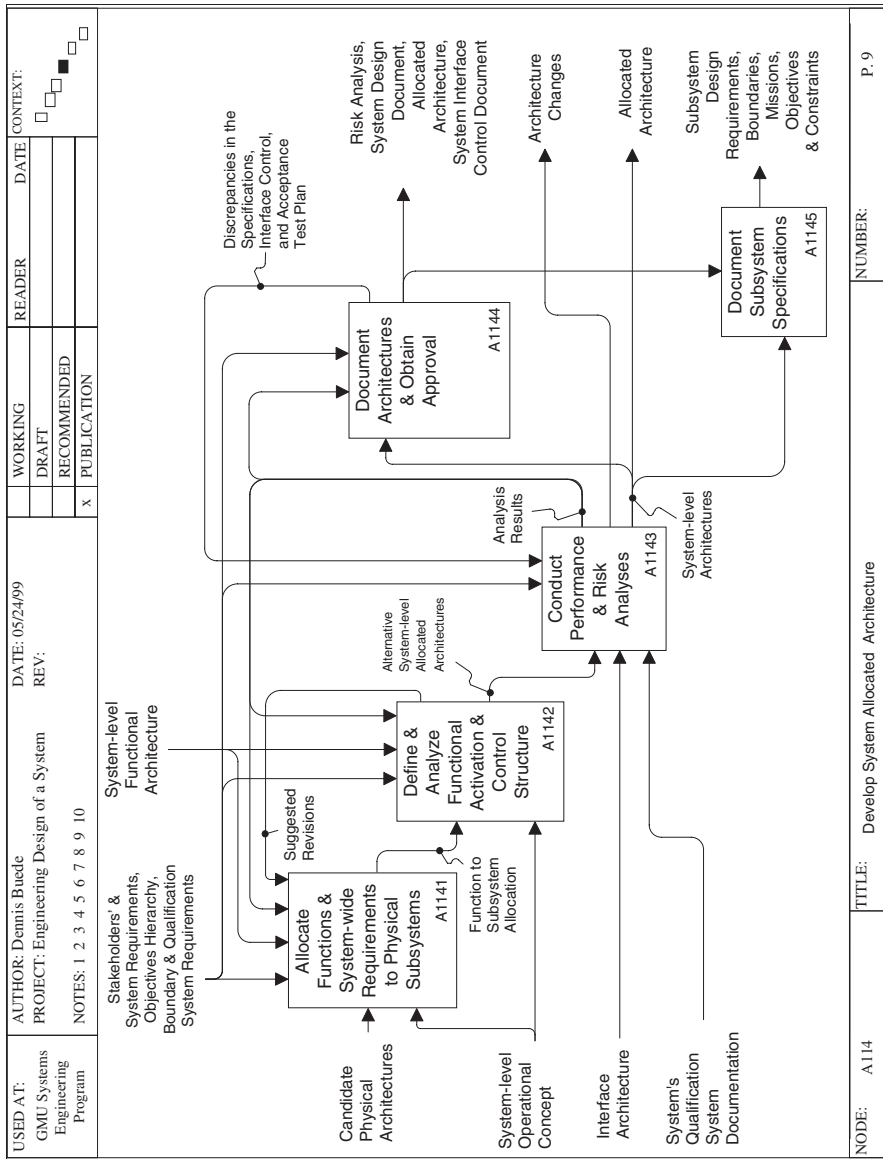










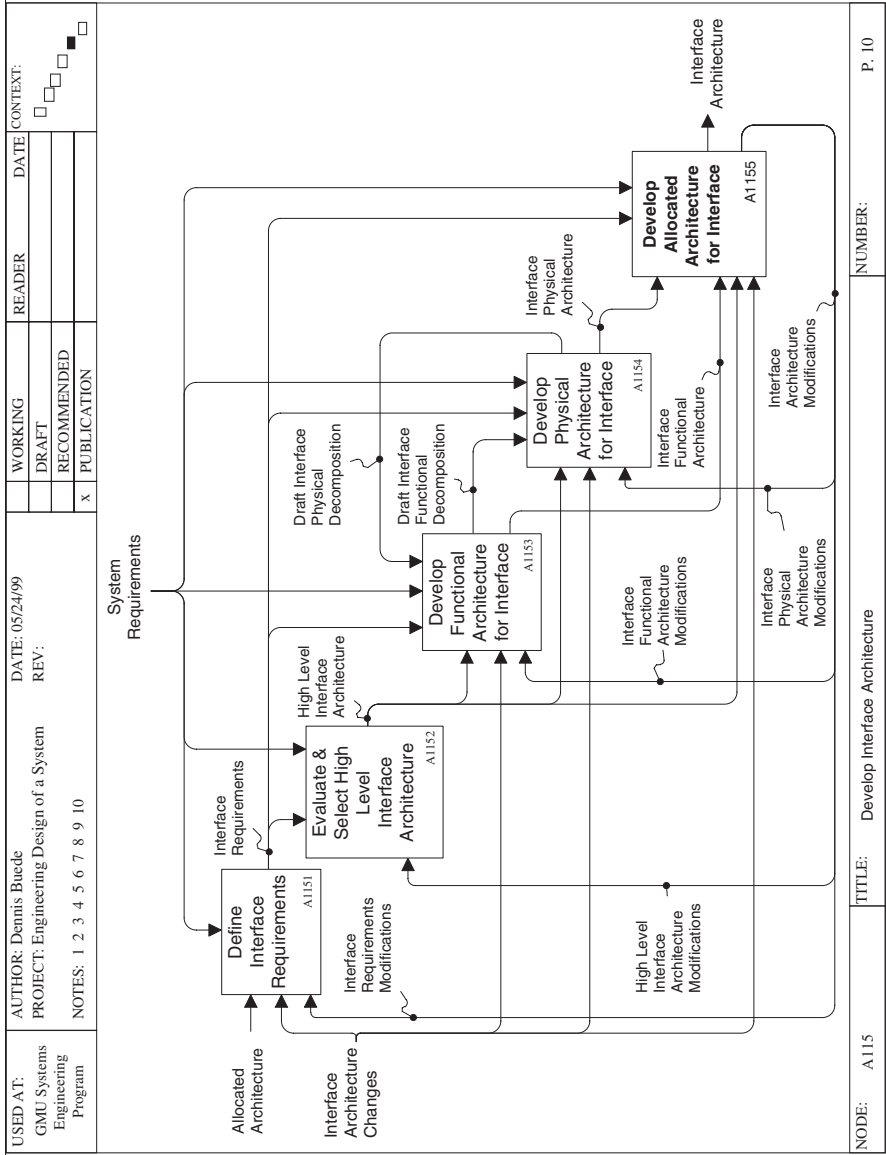


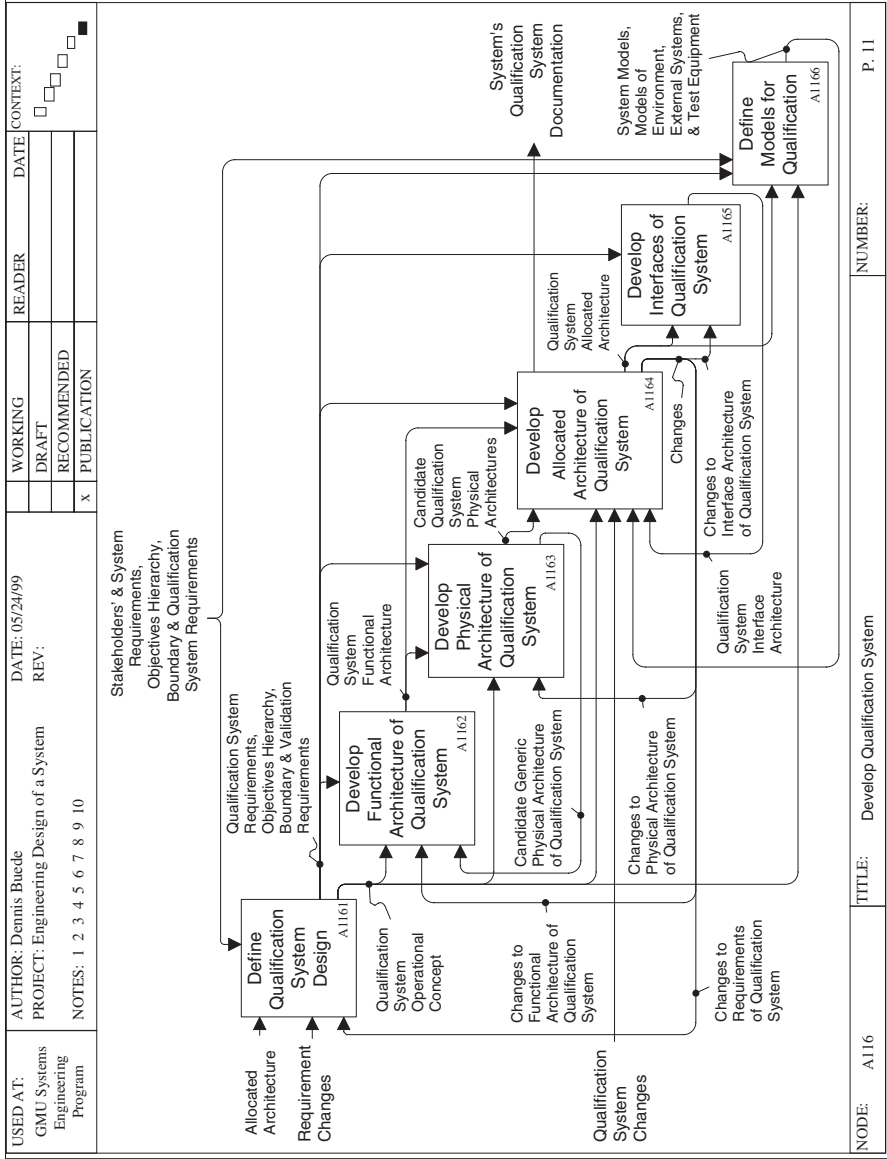
NODE: A114

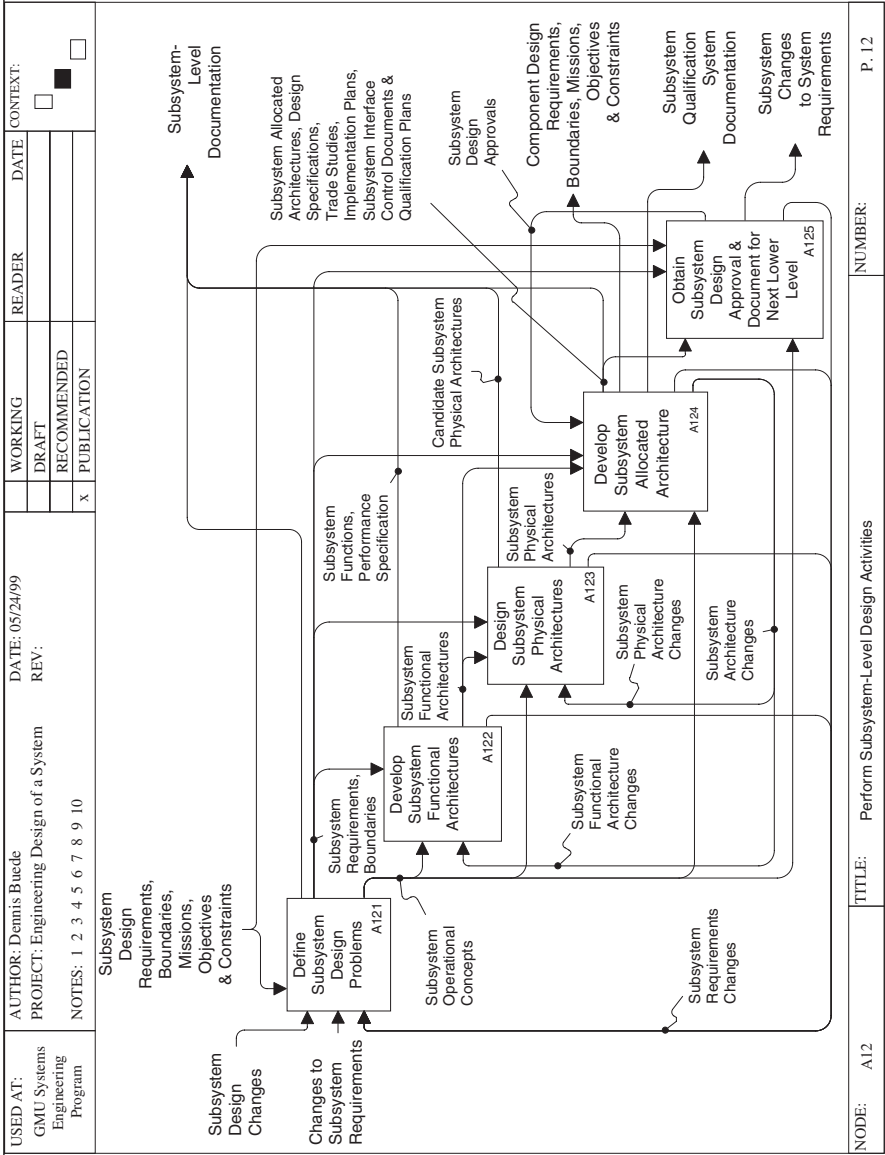
TITLE: Develop System Allocated Architecture

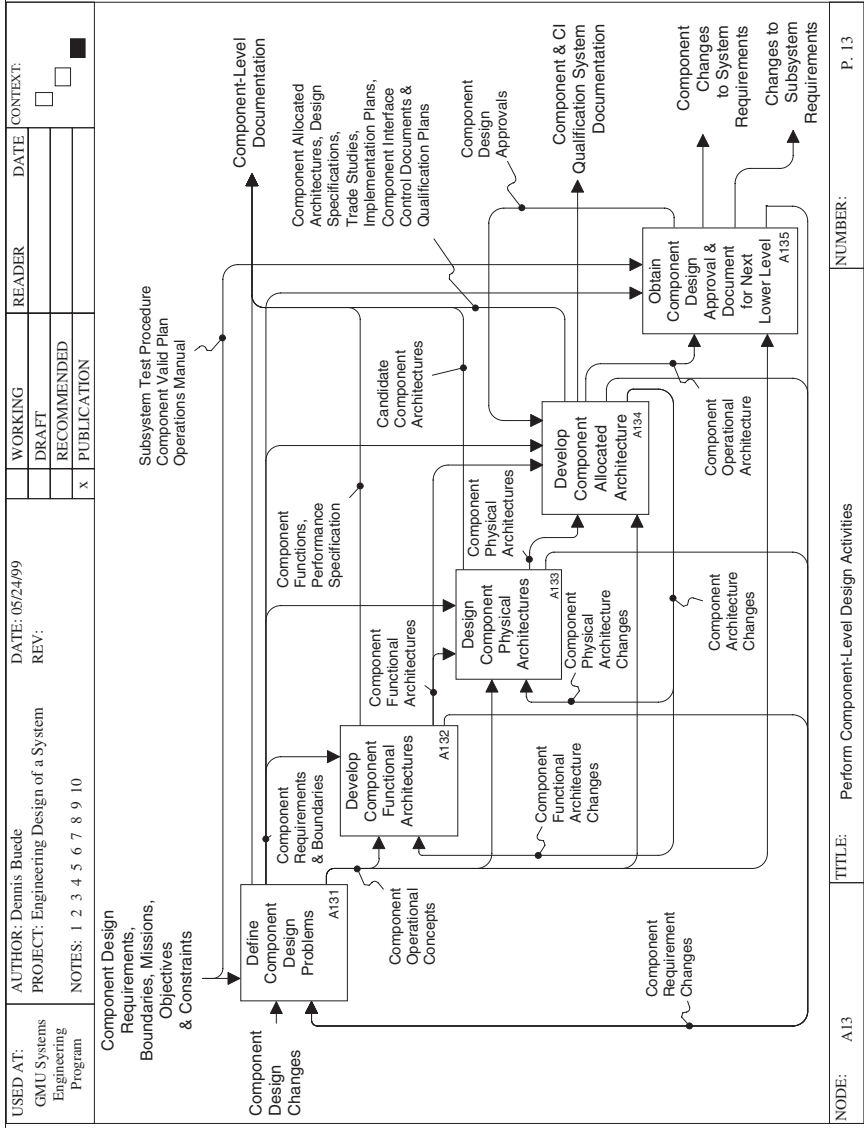
NUMBER:

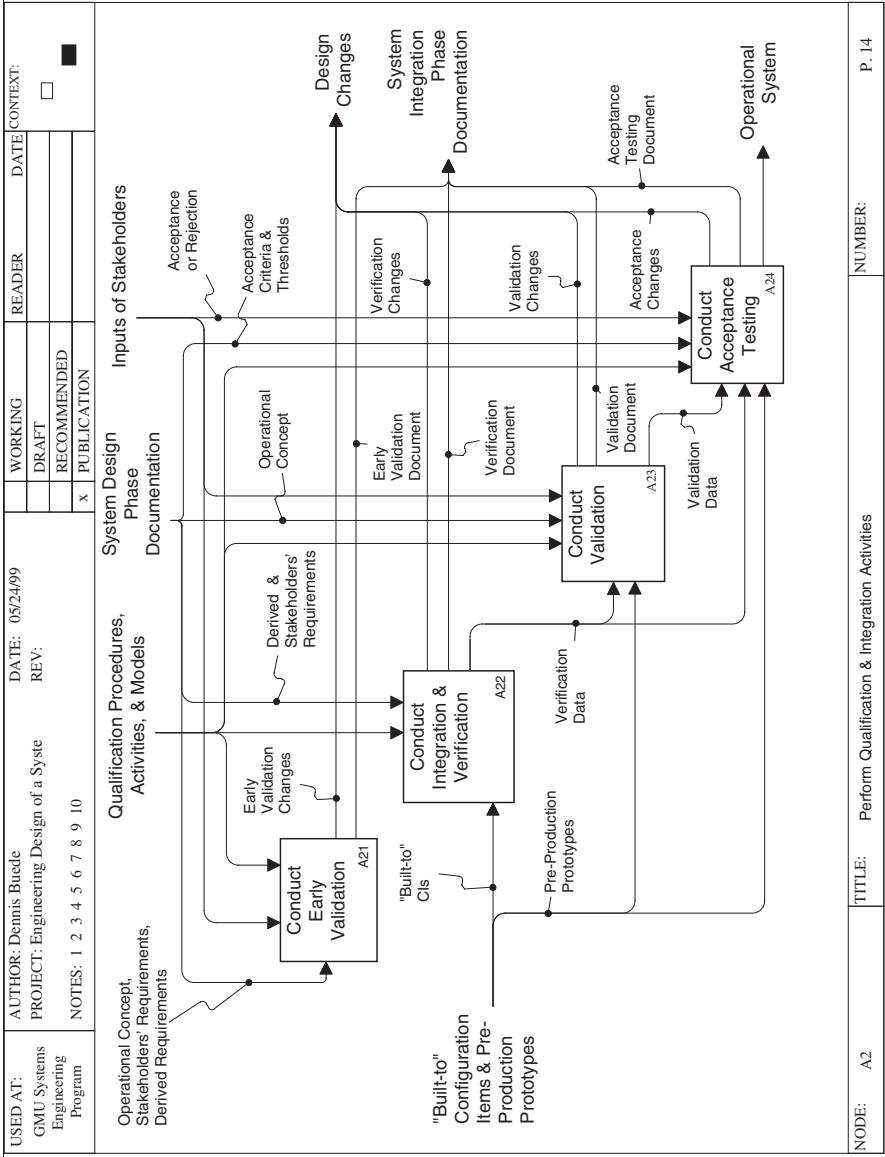
P. 9

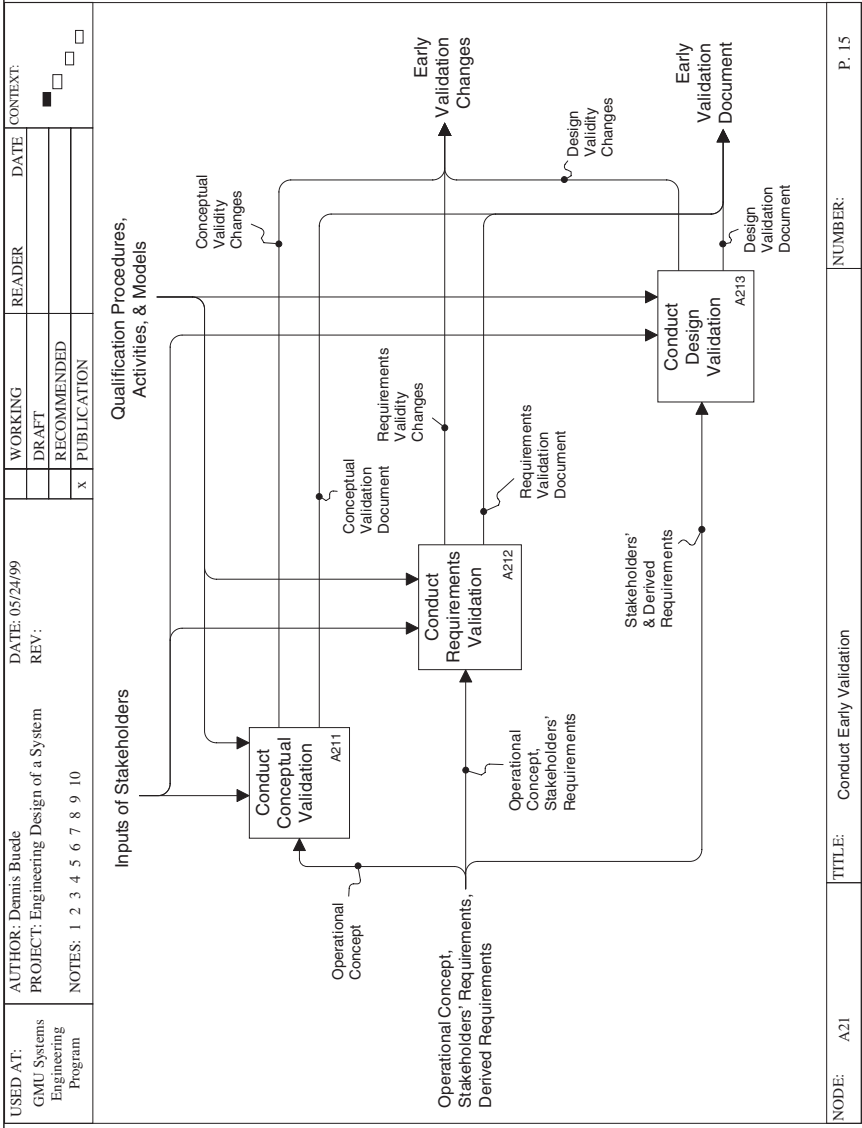












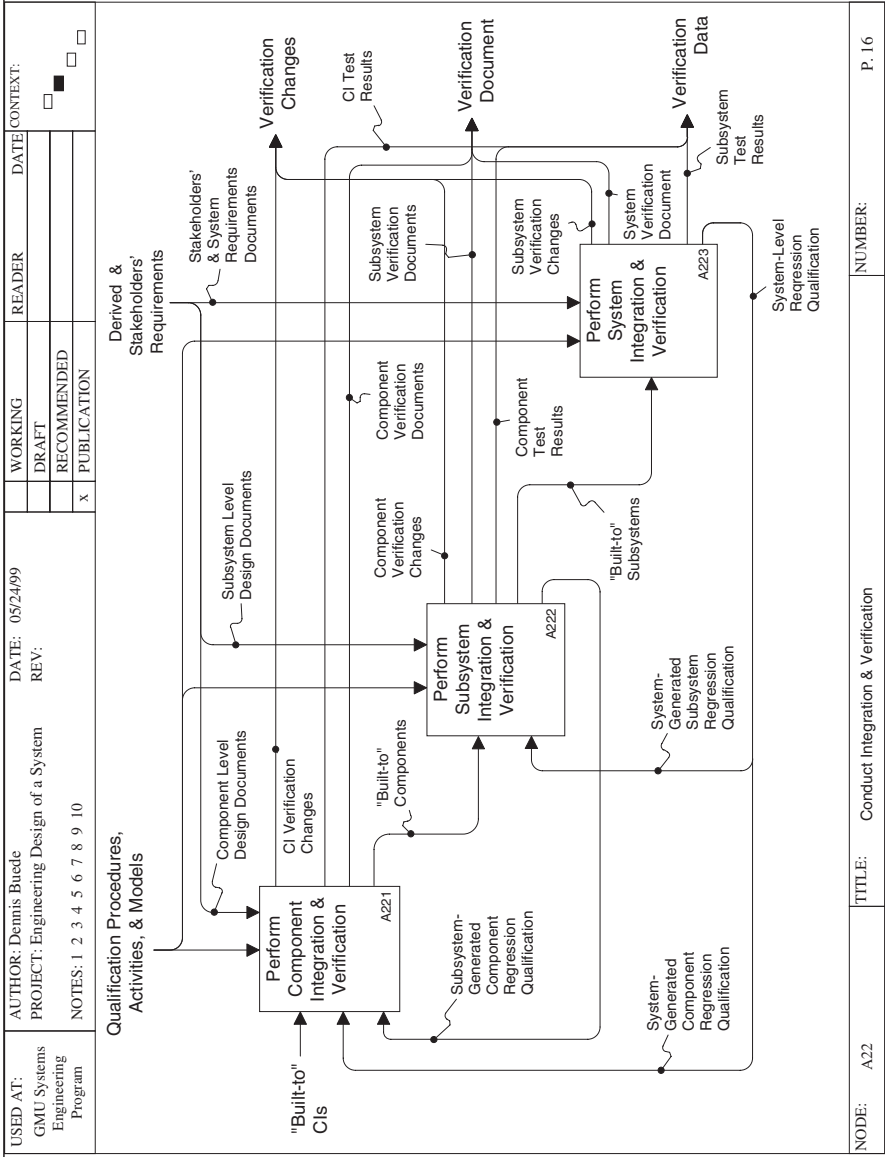
NODE: A21

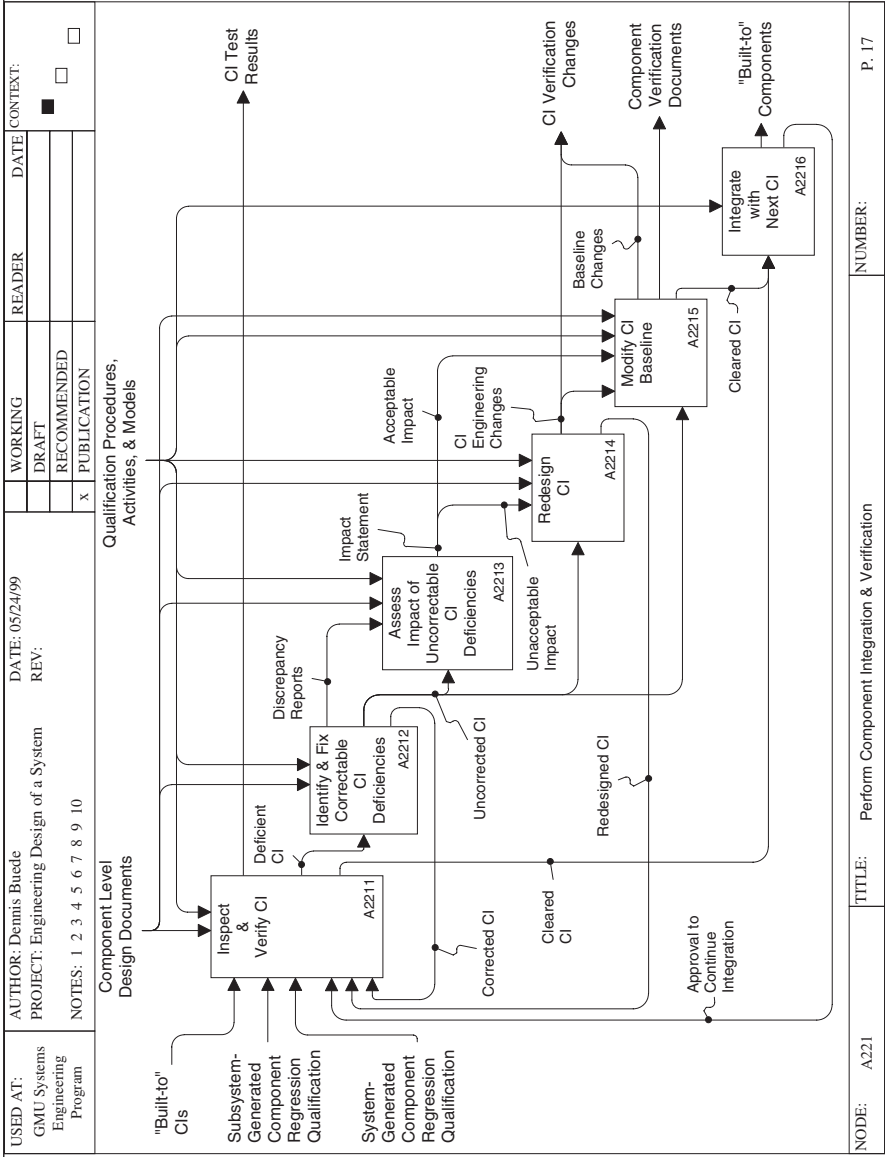
TITLE: Conduct Early Validation

NUMBER:

P. 15



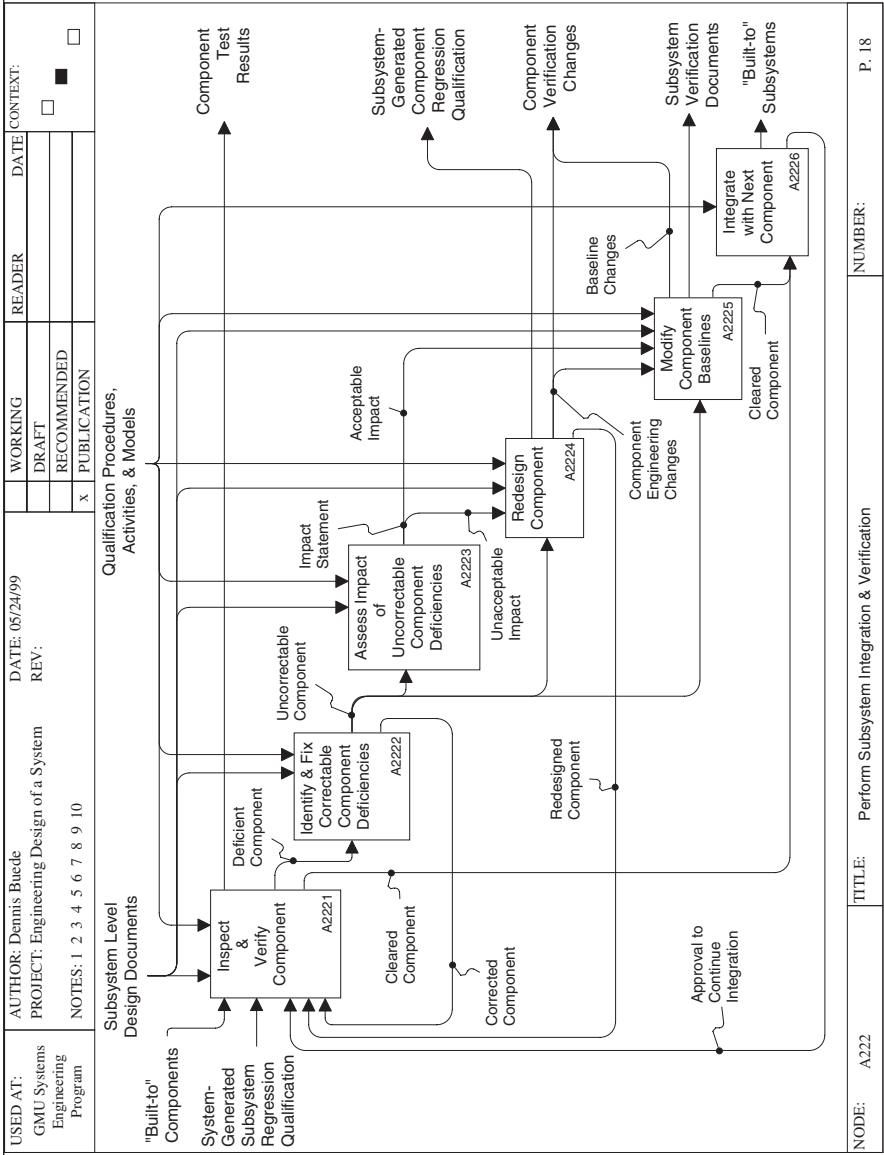




NODE: A221

TITLE: Perform Component Integration & Verification

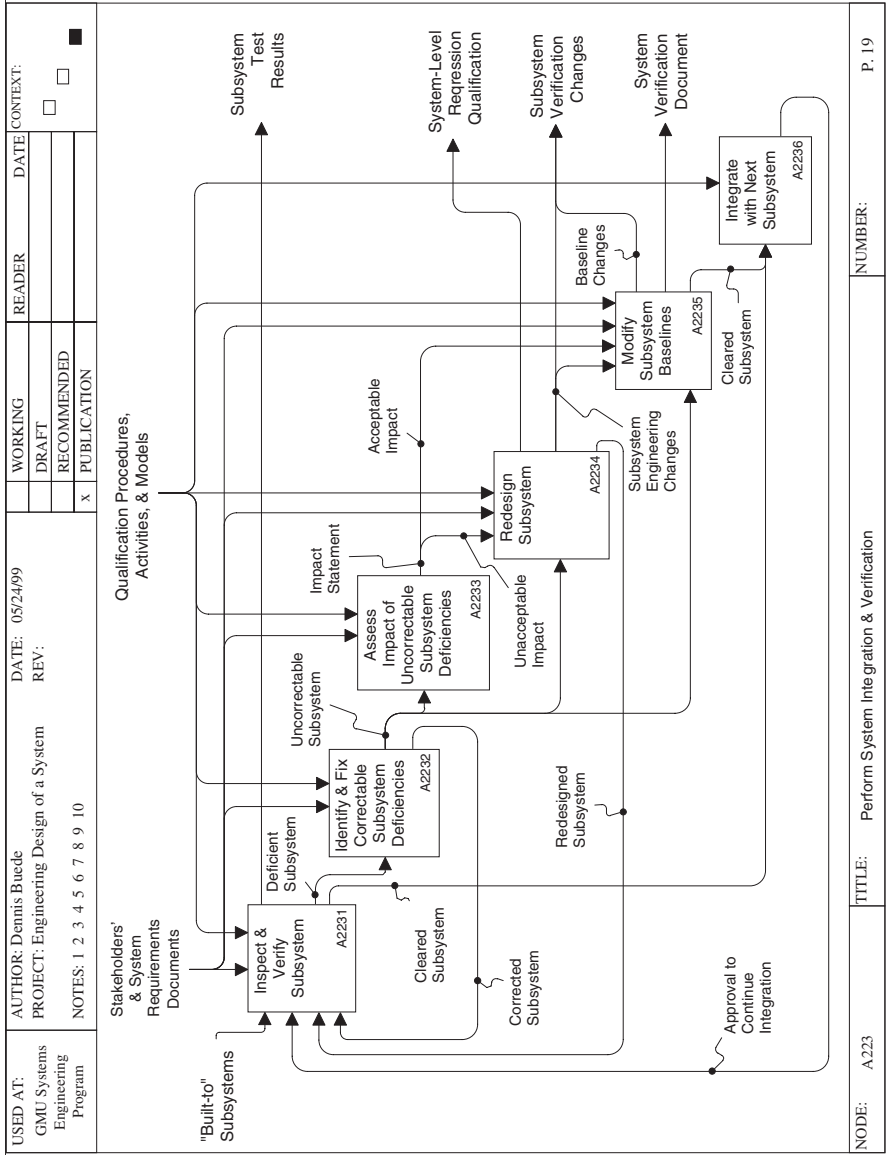
NUMBER: P. 17



NODE: A222

TITLE: Perform Subsystem Integration & Verification

NUMBER: P. 18



NODE: A223

TITLE: Perform System Integration & Verification

NUMBER: P. 19